



UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of : **Confirmation No. 1350**
Yasushi IMAMURA et al. : **Docket No. 2001_0244A**
Serial No. 09/786,482 : **Group Art Unit 2126**
Filed May 15, 2001 : **Examiner Lewis A. Bullock, Jr**
DIGITAL SIGNAL PROCESSOR

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

THE COMMISSIONER IS AUTHORIZED
TO CHARGE ANY DEFICIENCY IN THE
FEE FOR THIS PAPER TO DEPOSIT
ACCOUNT NO. 23-0975.

Sir:

Pursuant to the provisions of 37 CFR 1.56, 1.97 and 1.98, Applicants request consideration of the references listed on attached form PTO-1449 and any additional information identified below in paragraph 3. A legible copy of each reference listed on the Form PTO-1449 is enclosed, except a copy is not provided for:

- ☒ each U.S. Patent and U.S. Patent application publication;
- ☐ each reference previously cited in the international application
PCT/_____; and/or
- ☐ each reference previously cited in prior parent application Serial No.
_____.

1a. ☐ This Information Disclosure Statement is submitted:

within three months of the filing date (or of entry into the National Stage) of the above-entitled application, or

before the mailing of a first Office Action on the merits or the mailing of a first Office Action after the filing of an RCE,